

FIG. 1A is a block diagram of a system architecture.

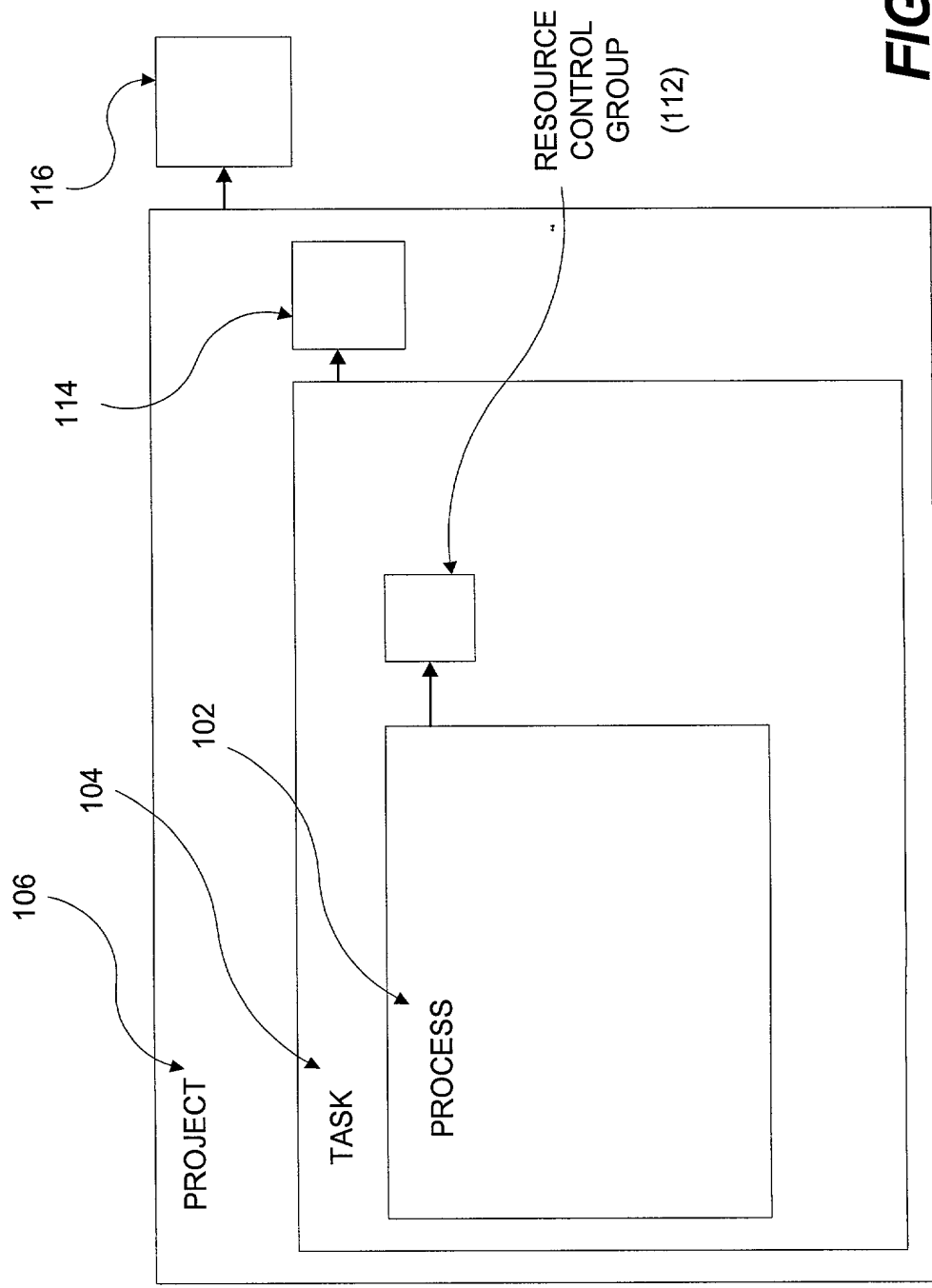
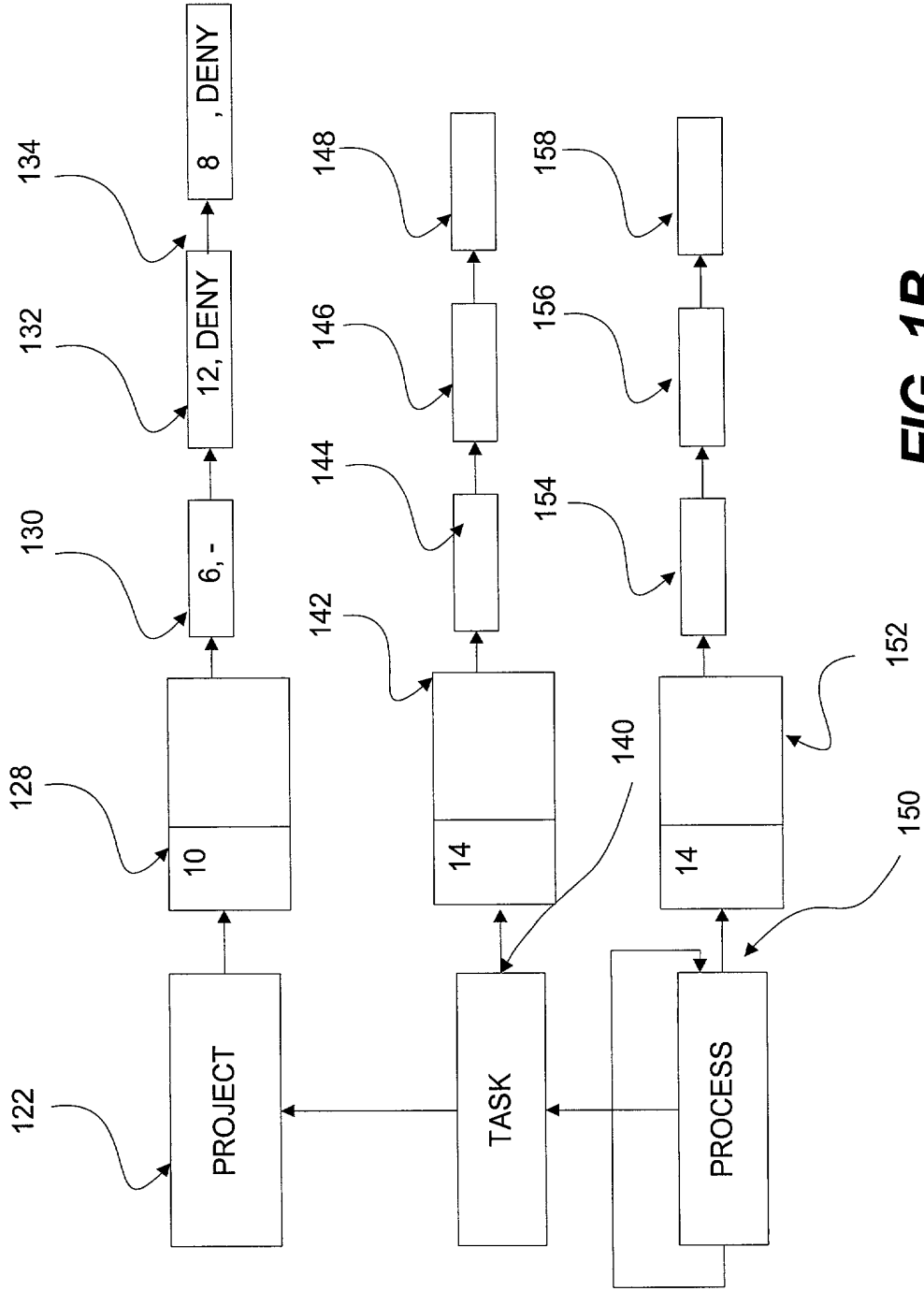
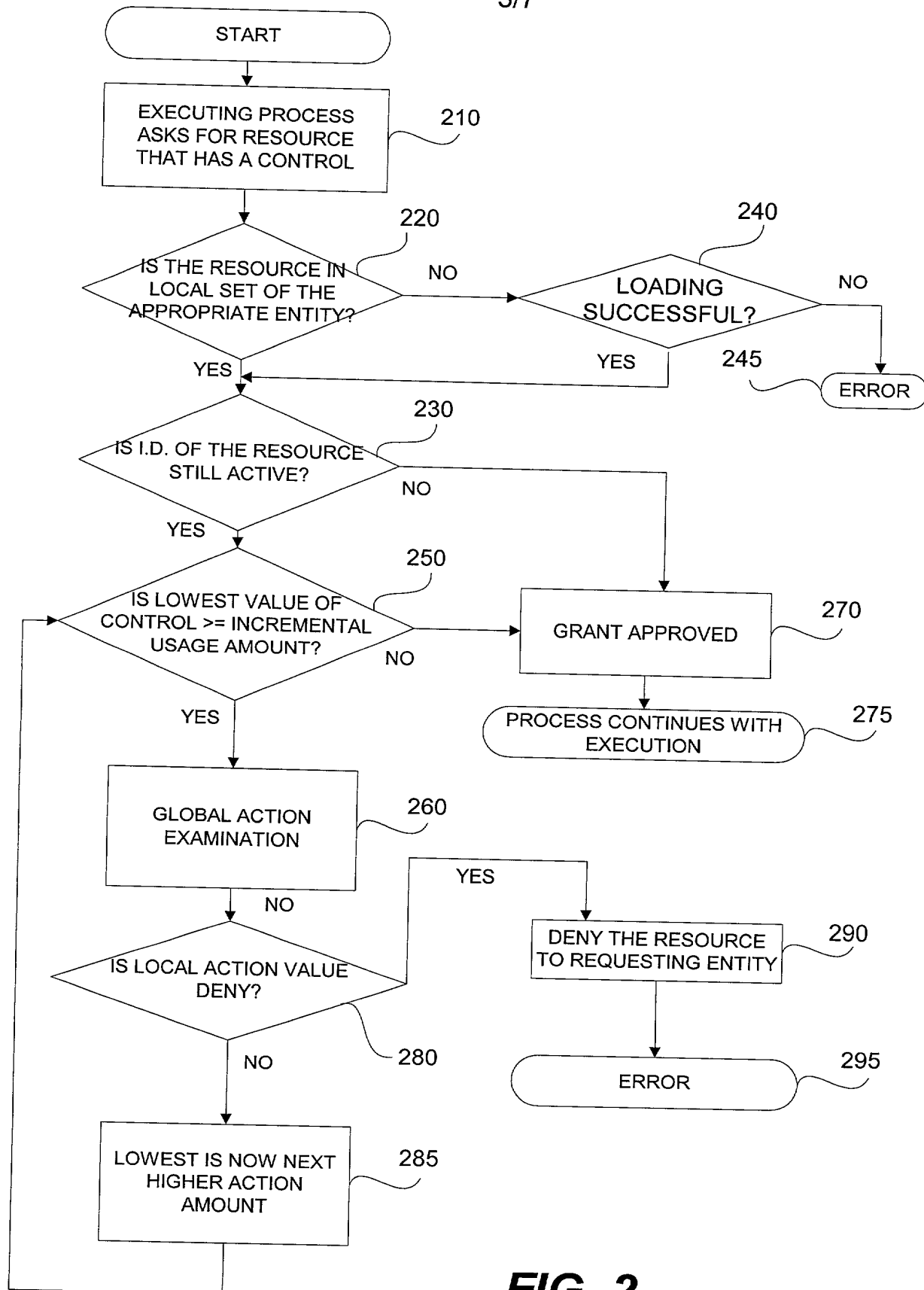
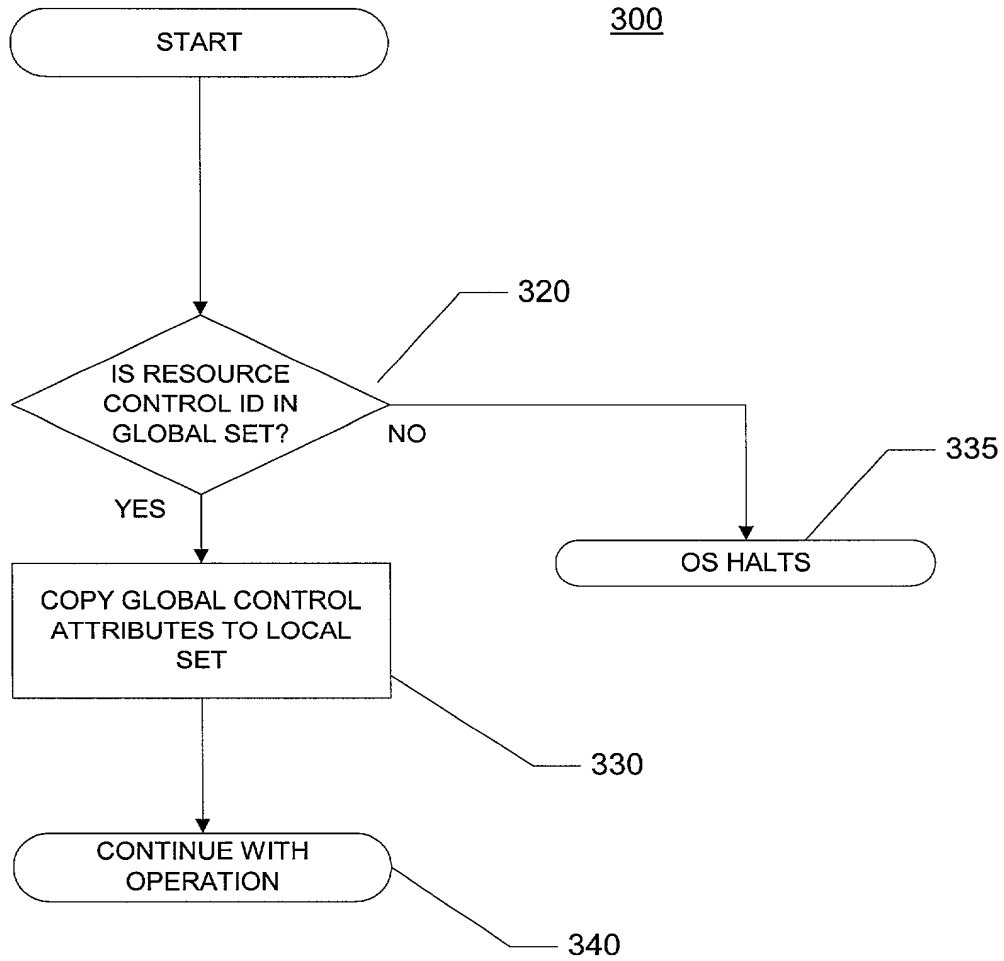


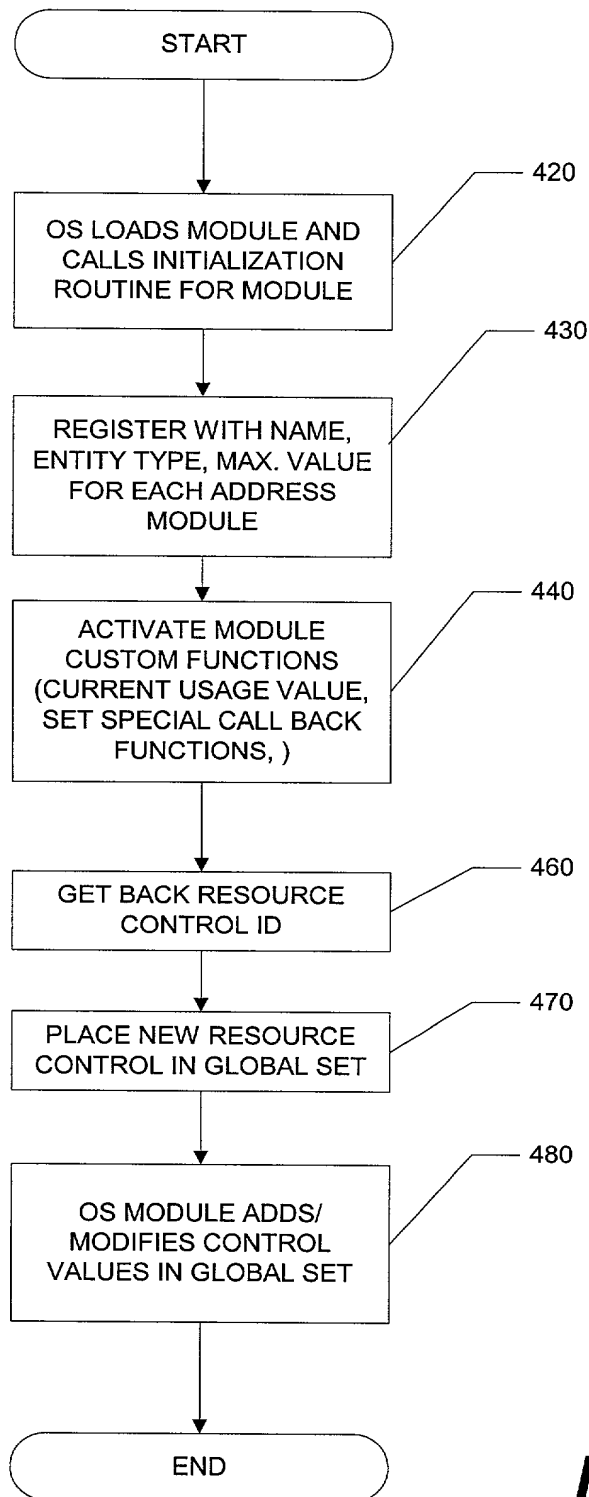
FIG. 1A

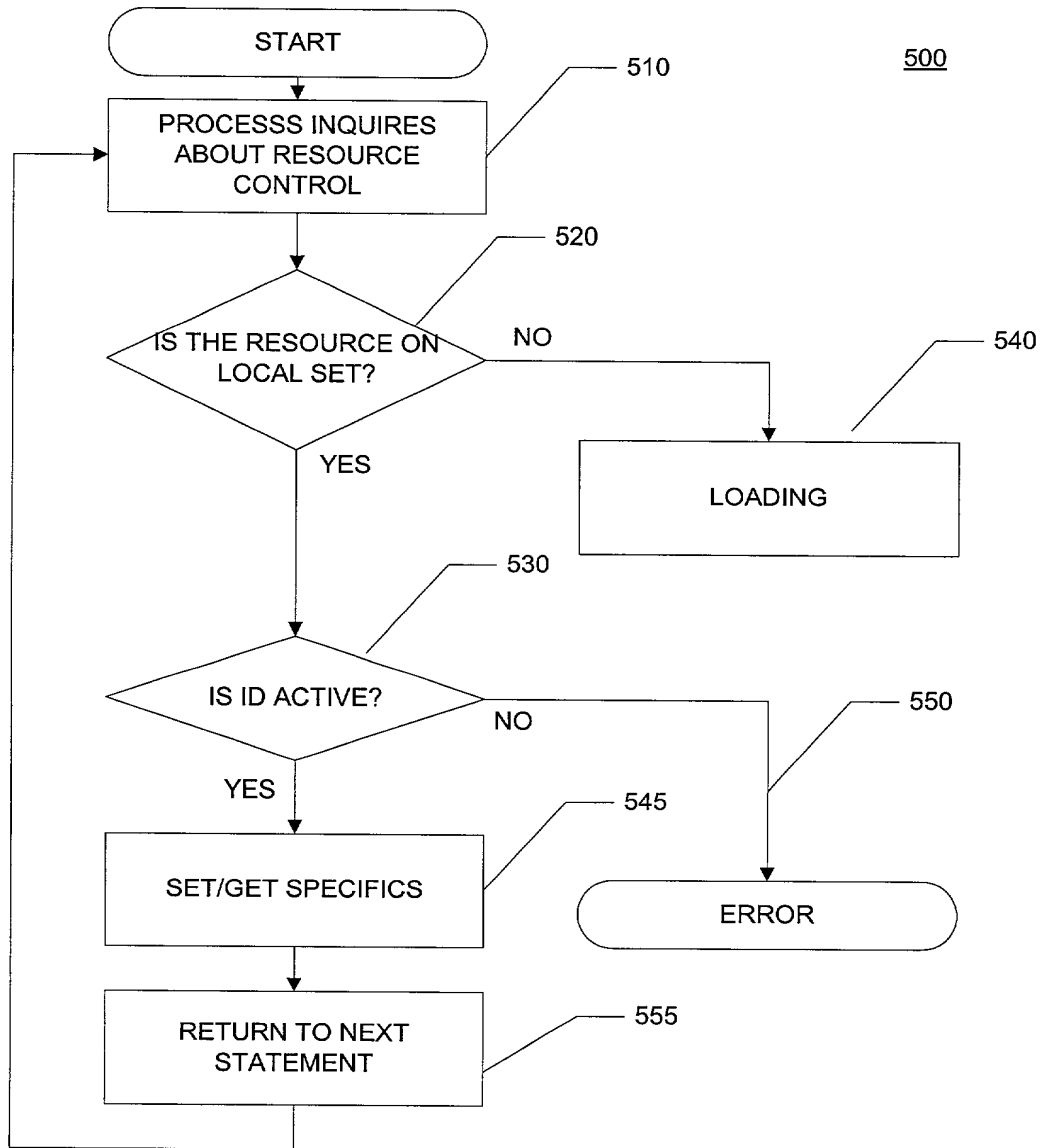


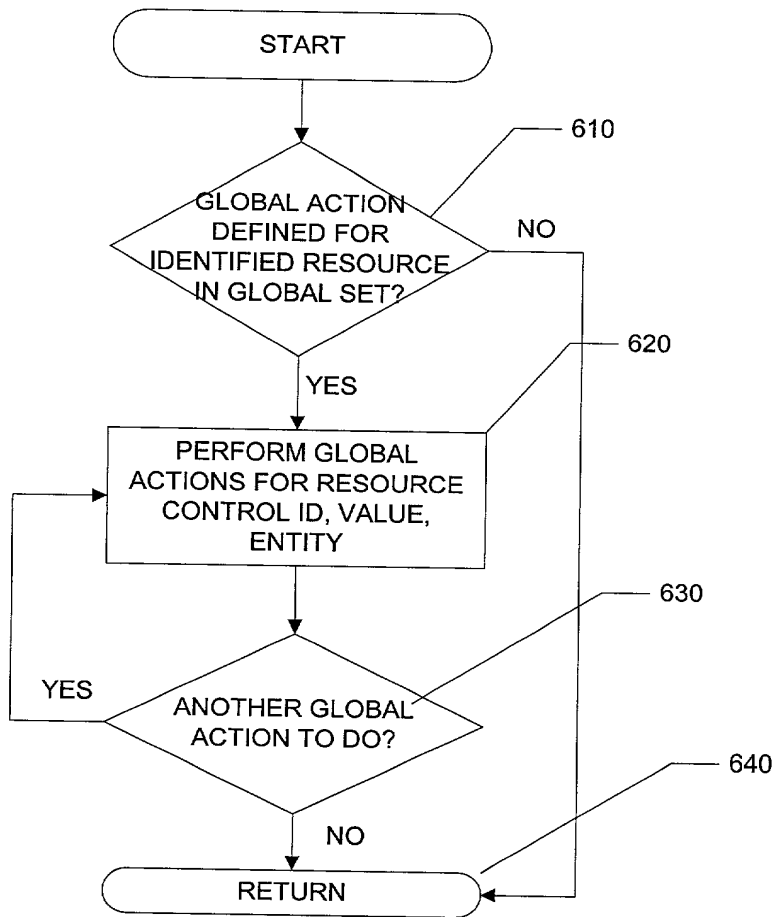
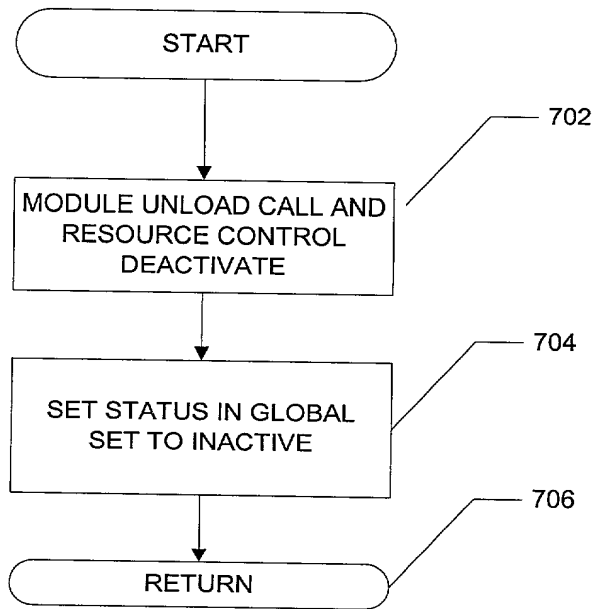
**FIG. 1B**

**FIG. 2**

**FIG. 3**

400**FIG. 4**

**FIG. 5**

**FIG. 6****FIG. 7**